

ABSTRACT OF THE DISCLOSURE

An image processing apparatus operates with a high-speed print engine. An ASIC is provided between a graphics port and a peripheral device interconnection port. The print engine is connected to the peripheral device interconnection port. A memory is provided on a side of the CPU with respect to the graphics port. A CPU processes image data and stores the image data in the memory. The CPU transfers the image data stored in the memory to the print engine directly through the graphics port, the ASIC and the peripheral device interconnection port.

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